

Short Bios of the Presenter

Dr. Hasan Mahmood Aminul Islam is an Assistant Professor in the Department of Computer Science and Engineering (CSE) at East West University in Dhaka, Bangladesh. He earned a Doctor of Science degree in Technology (CSE) from the School of Science at Aalto University, Finland, in 2018. Previously, he worked as a Specialist in System-on-Chip (SoC) Software at Nokia headquarters in Espoo, Finland, where he developed Layer 1 Low (llow) software drivers for 5G technology from 2019 to 2022. During his doctoral studies, he played a key role in the EU-H2020 POINT Project, which focused on future internet architecture (ICN). For his master's thesis, he collaborated with the Nomadic Laboratory at L.M. Ericsson in Finland, working closely with the IETF community on data channel protocol options for RTCWeb. His research interests include data communications and distributed systems, Internet protocols, IoT protocols, Future Internet architecture, Information-Centric networking, and Delay-Tolerant Networking.

Md. Khalid Mahbub Khan received the B.Sc. (Eng.) and M.Sc. (Eng.) degrees in information and communication engineering from the University of Rajshahi, Bangladesh, in 2015 and 2017, respectively. He was a Lecturer in the Department of Computer Science and Engineering at the University of Information Technology and Sciences (UITS), Dhaka, Bangladesh, from 2019 to 2022. Currently, he is a Lecturer in the Department of Computer Science and Engineering at East West University, Dhaka. He is a Coordinator of the research group under the supervision of Dr. Hasan Mahmood Aminul Islam. His research interests include OPPORTUNISTIC NETWORKING, the Internet of Things, internet protocols, and IoT security.

Dr Michael Georgiades is an Assistant Professor at Neapolis University of Pafos. He holds a BEng degree in Communications and Radio Engineering from King's College London in 2000 (First Class Honours), an MSc degree in Telecommunications at University College London in 2001, and a PhD in Wireless and Mobile Networks from the University of Surrey in 2008. Past positions held include R&D Manager at Primetel PLC (Cyprus), Research Fellow at the Centre of Communication Systems Research (CCSR), University of Surrey (UK), and Systems Development Engineer at INSIG Ltd (UK). He has been involved in many EU-funded projects in the field of ICT. He is a patent holder and has published in numerous prestigious international journals, conferences, and books, as well as contributed to several IETF standards in the area of ICT. He has received multiple awards, including the Nokia Prize for Research Excellence, Primetel Awards for Excellence and Contribution, as well as EPSRC and British Council studentship awards. Areas of research interests include: Tactile Internet, Mobile Edge Computing, Vehicular Networks, Internet of Things, Edge Intelligence.

Dr. Riadul Islam is currently an assistant professor in the Department of Computer Science and Electrical Engineering at the University of Maryland, Baltimore County. In his Ph.D. dissertation work at UCSC, Riadul designed the first current-pulsed flip-flop/register that resulted in the first-ever one-to-many current-mode clock distribution networks for high-performance microprocessors. From 2017 to 2019, he was an Assistant Professor with the University of Michigan, Dearborn, MI, USA. From 2007 to 2009, he worked as a full-time faculty member in the Department of Electrical and Electronic Engineering of the University of Asia Pacific, Dhaka, Bangladesh. He is a member of the ACM, IEEE,

IEEE Circuits and Systems (CAS) society, and IEEE Solid-State Circuits (SSC) Society. He is a member of the Cybersecurity Center for Research, Education, and Outreach at the UM-Dearborn. He holds two US patents and several IEEE/ACM/Springer Nature journal and conference publications in TVLSI, TCAS, JETTA, DAC, ISCAS, MWSCAS, ISQED, and ASICON. His current research interests include digital, analog, and mixed-signal CMOS ICs/SOCs for a variety of applications; verification and testing techniques for analog, digital, and mixed-signal ICs; hardware security; CAN network; CAD tools for design and analysis of microprocessors and FPGAs; automobile electronics; and biochips. He is an editorial board member of the Semiconductor Science and Information Devices journal.